

FIG. 1

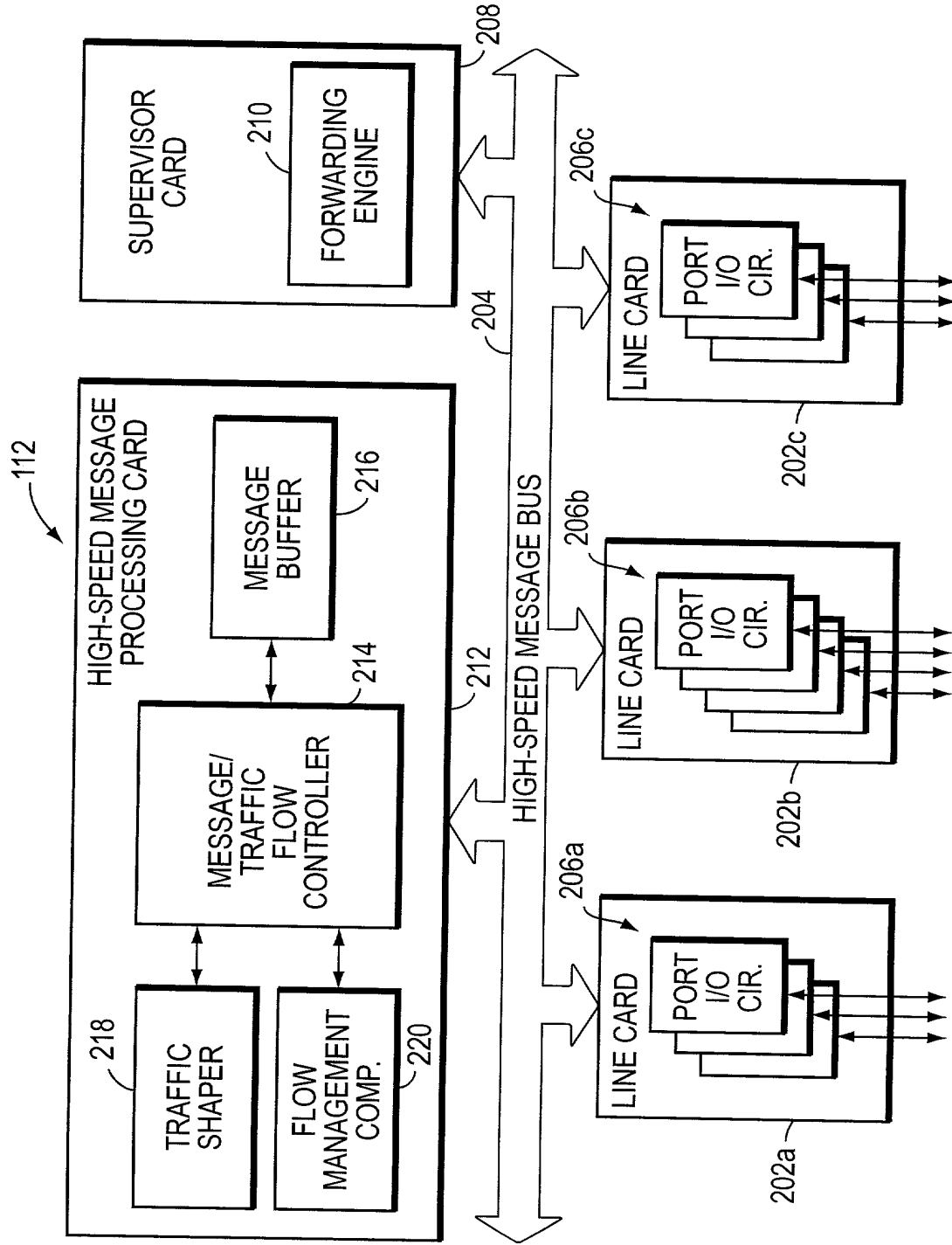


FIG. 2

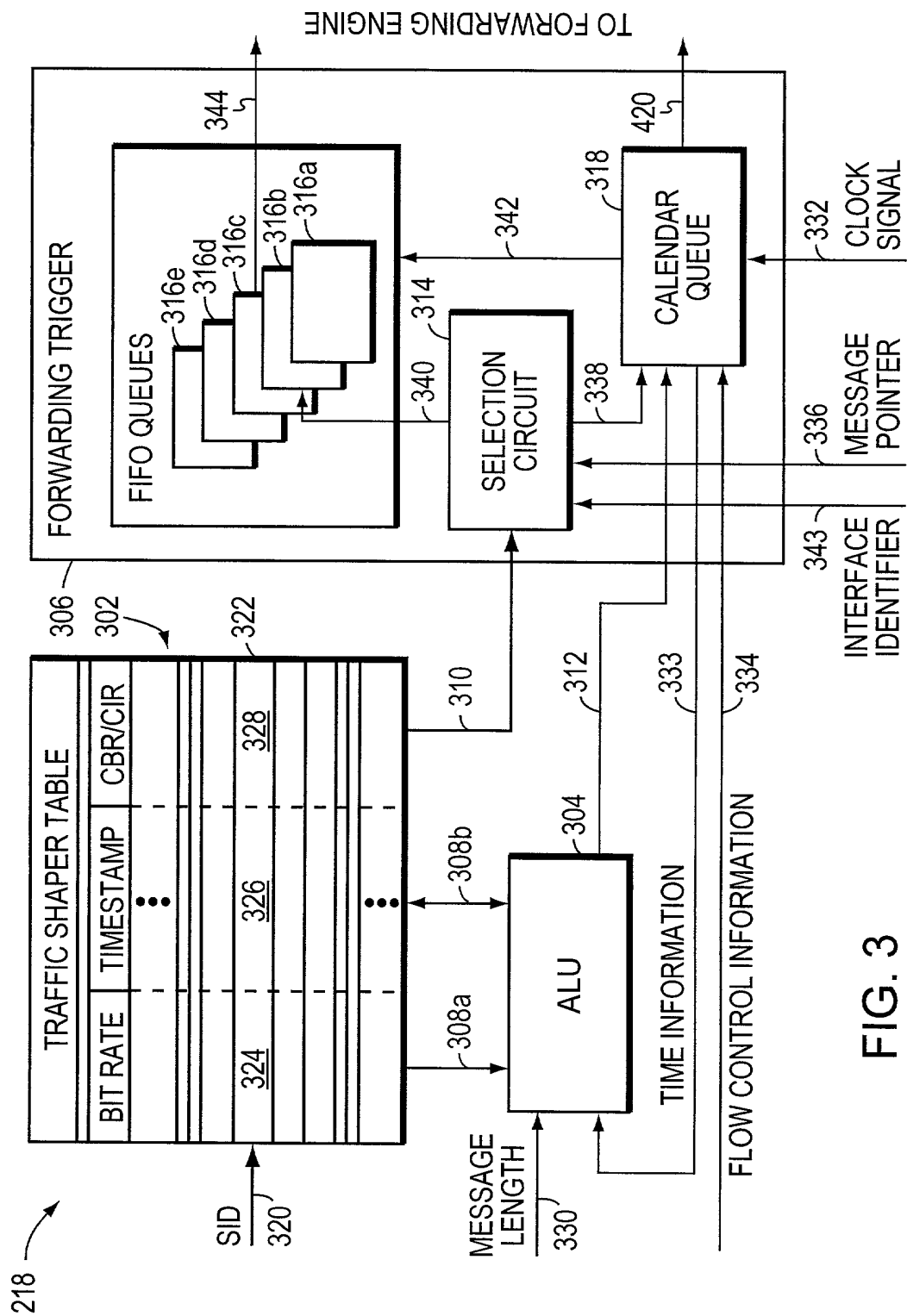
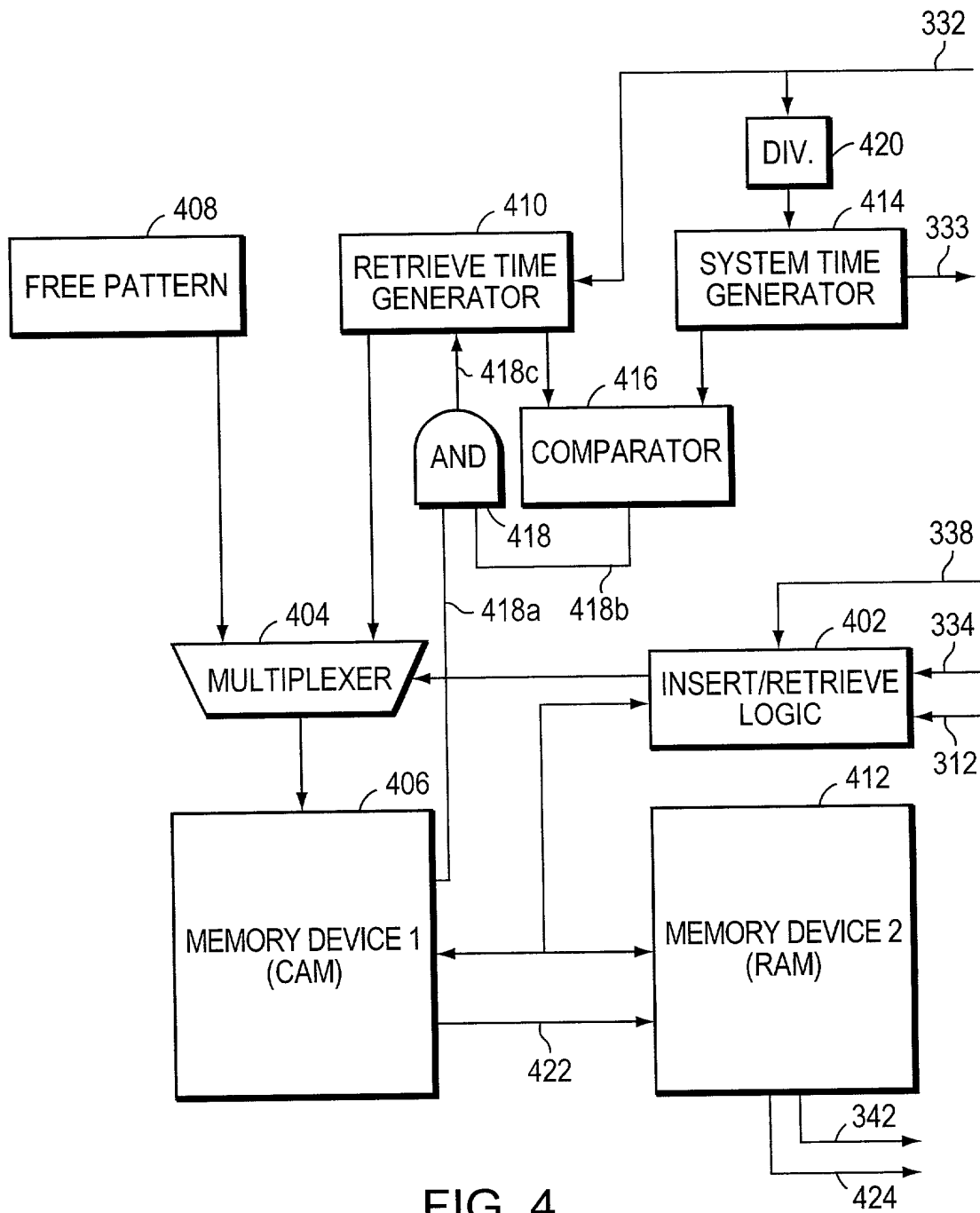


FIG. 3



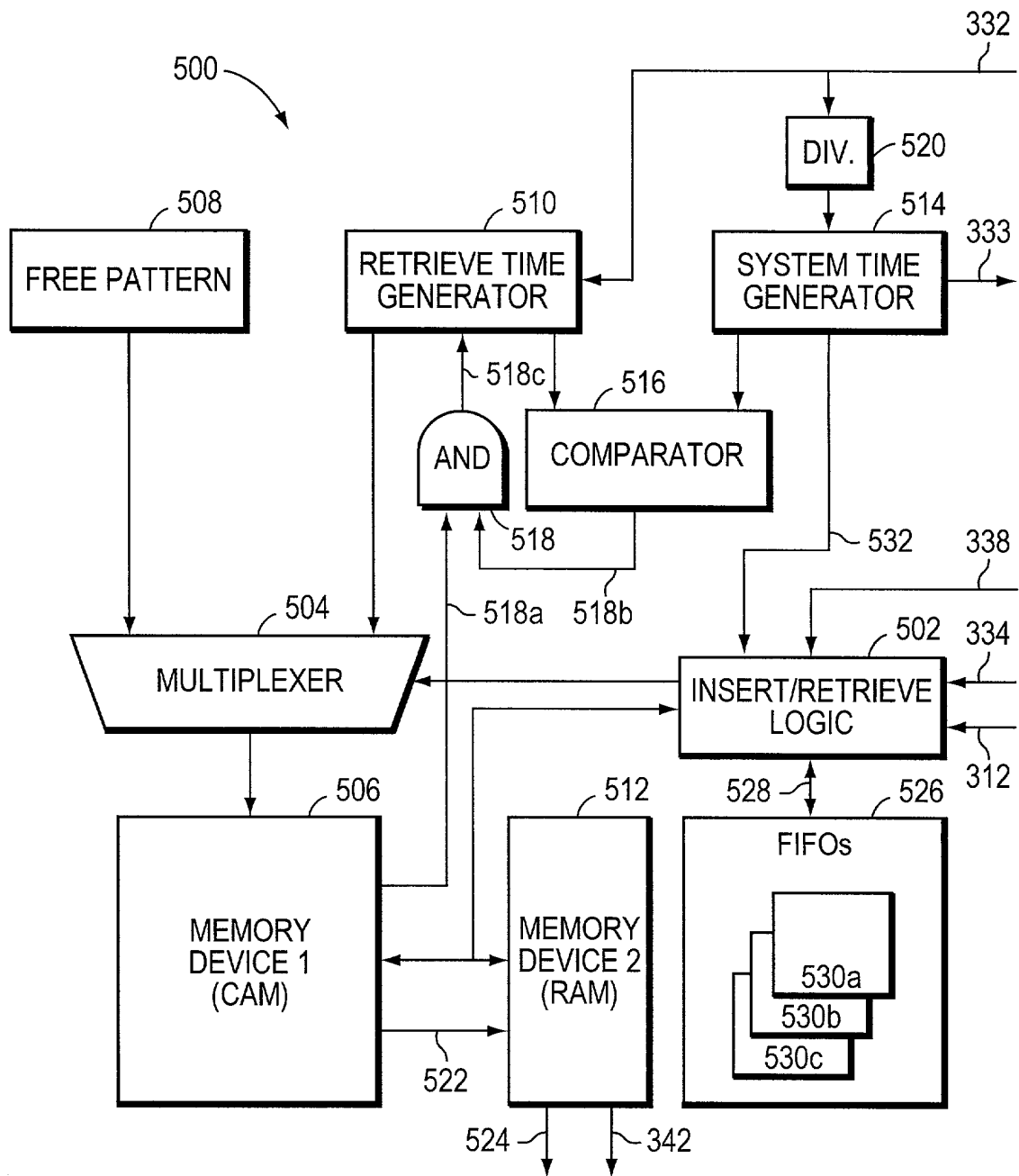


FIG. 5

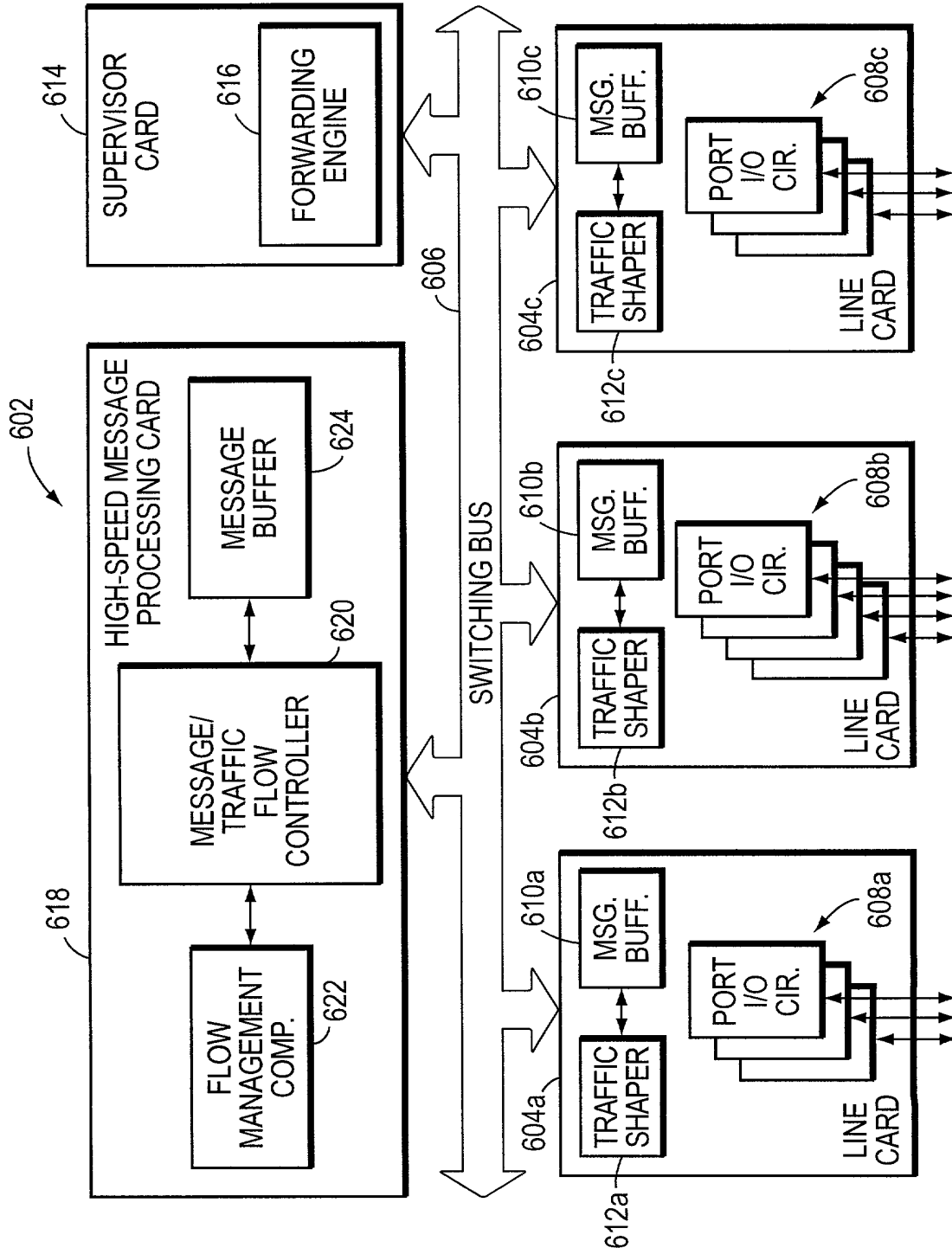


FIG. 6

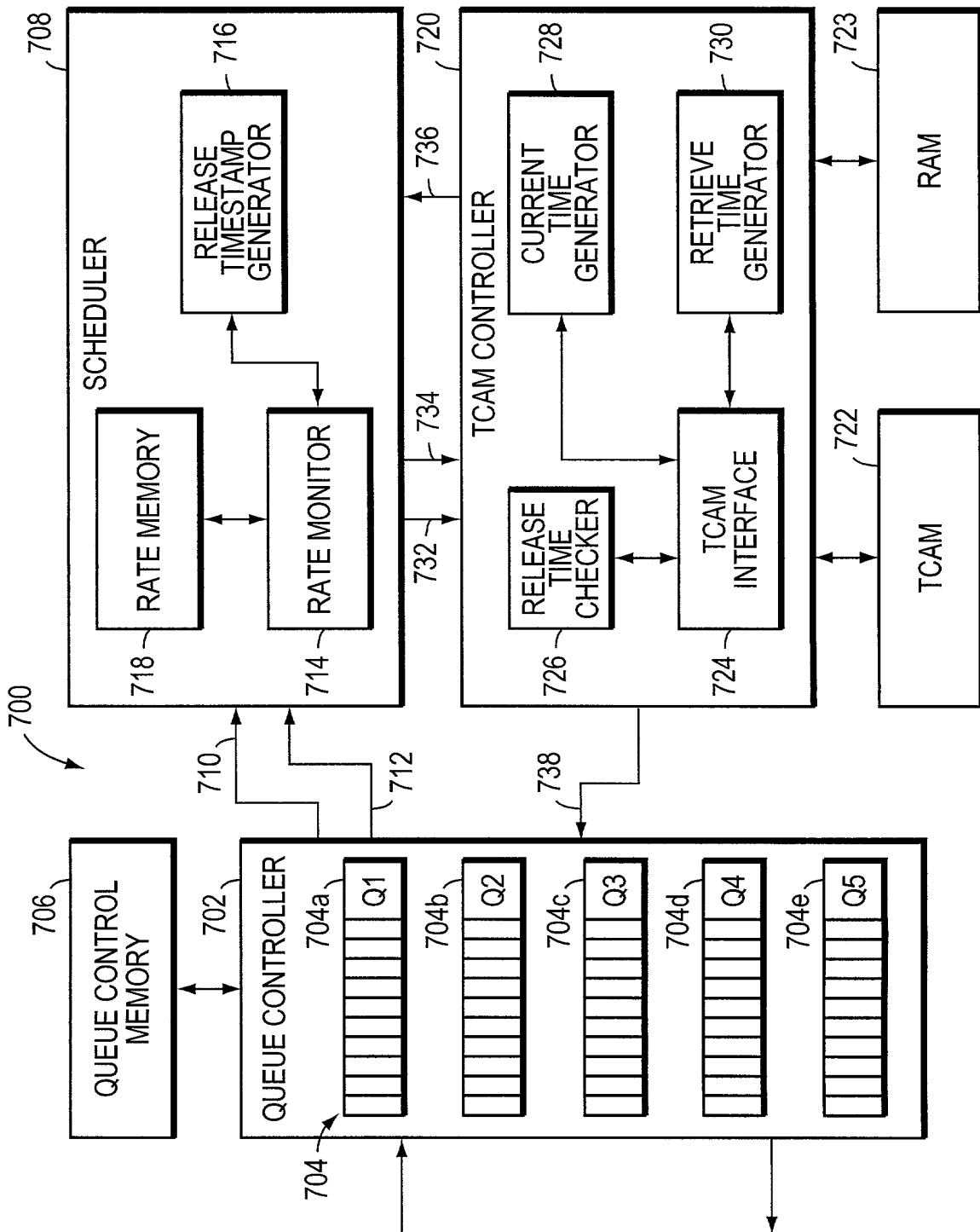


FIG. 7

706

	802	804	806	808	810	812
	QUEUE CONTROL MEMORY					
	SID	Q No.	Q TIME_REQ	HEAD PTR.	TAIL PTR.	NEXT SID
814a		Q1	0			
814b		Q2	0			
814c		Q3	1			
814d		Q4	1			
814e		Q5	0			
814f		Q6	1			
814g		Q7	1			
814h		Q8	1			

FIG. 8

714

	902	904	906	908	910	912	914	916
	RATE MEMORY							
	NEXT SID	LAST UPDATED TIME	DRAIN RATE	LAST UPDATED LEVEL	HIGH WATER MARK THRESHOLD	LOW WATER MARK THRESHOLD	CIR RATE	EIR RATE
918a								
918b								
918c								
918d								
918e								
918f								
918g								
918h								

FIG. 9

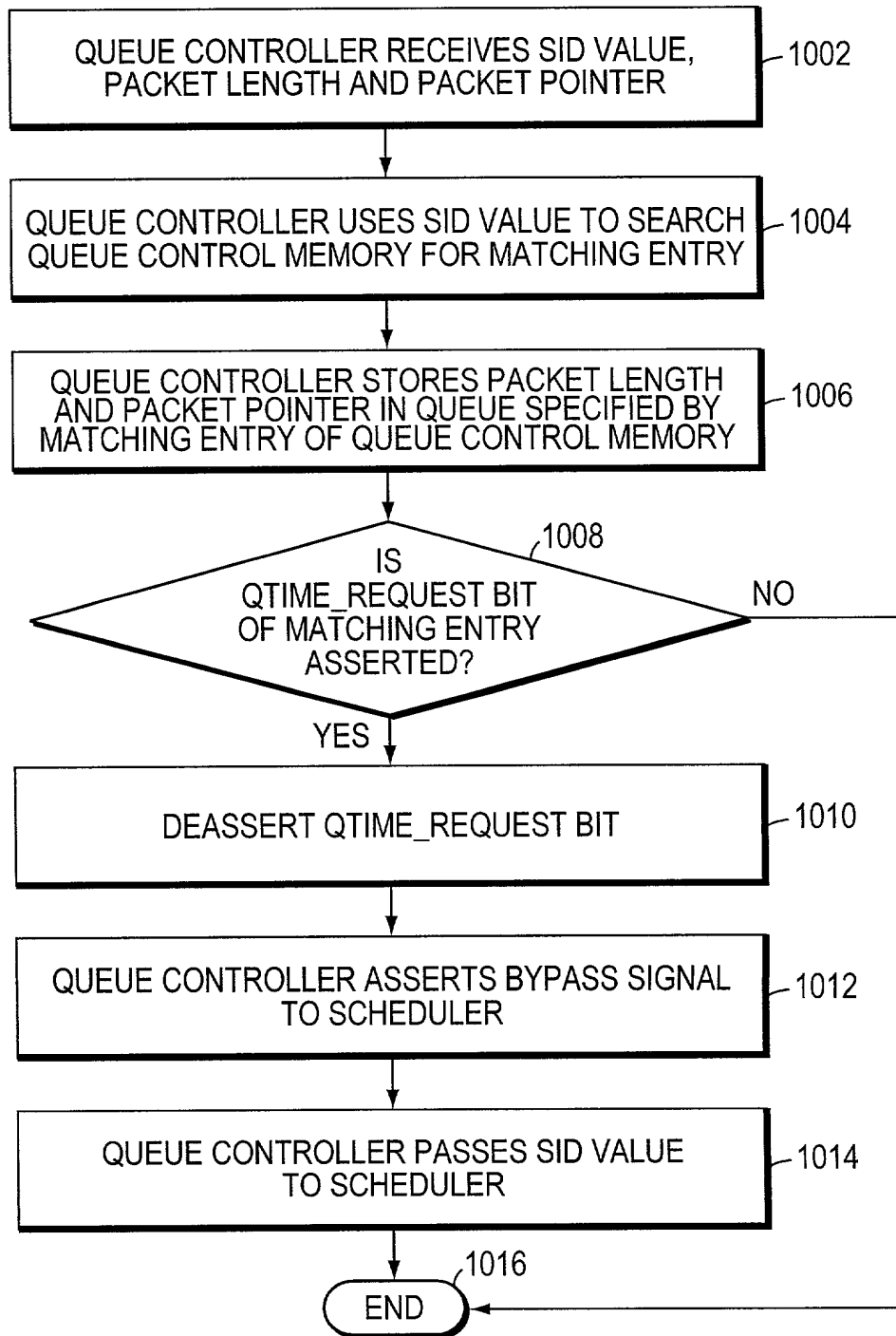


FIG. 10A

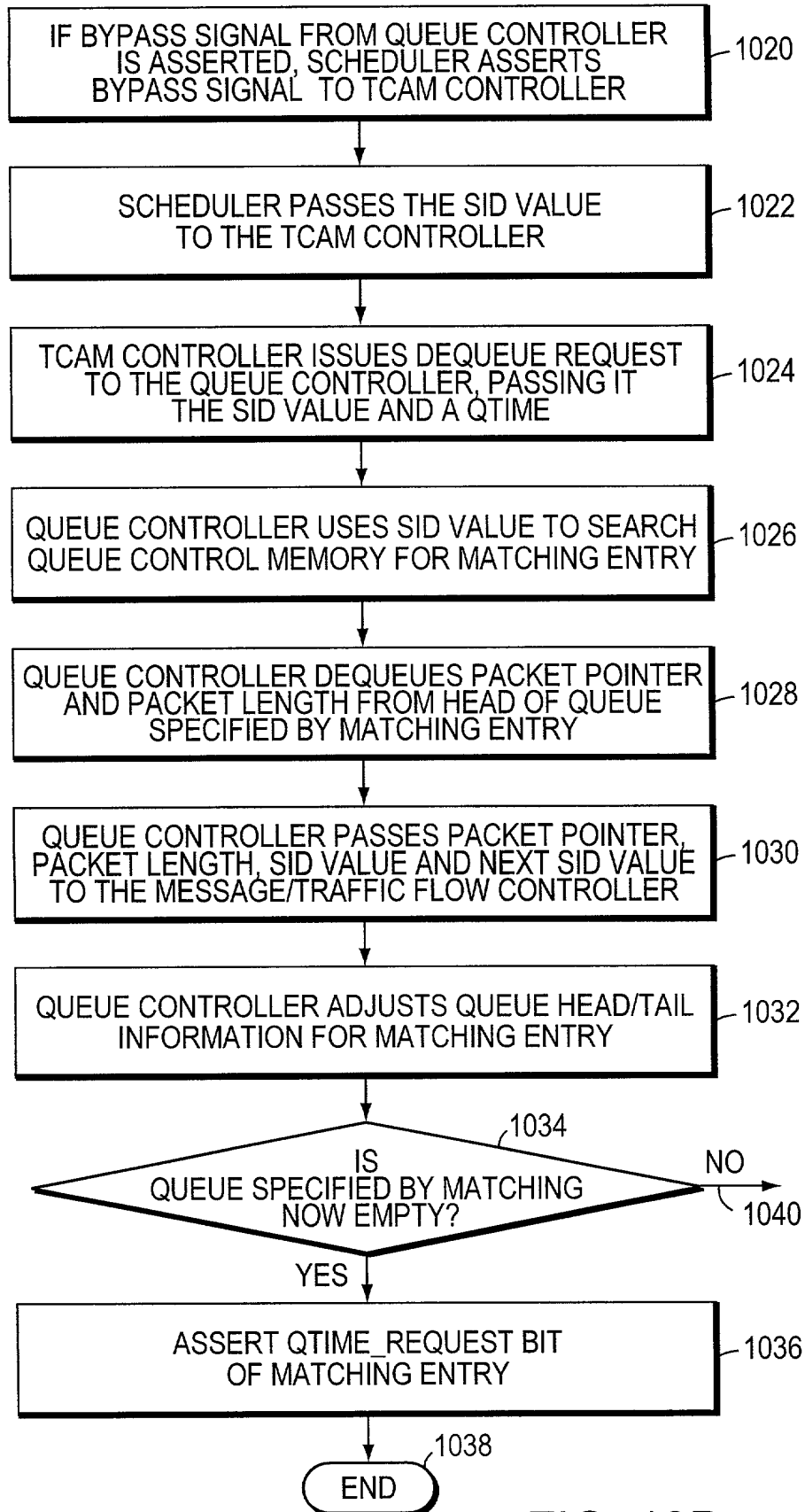


FIG. 10B

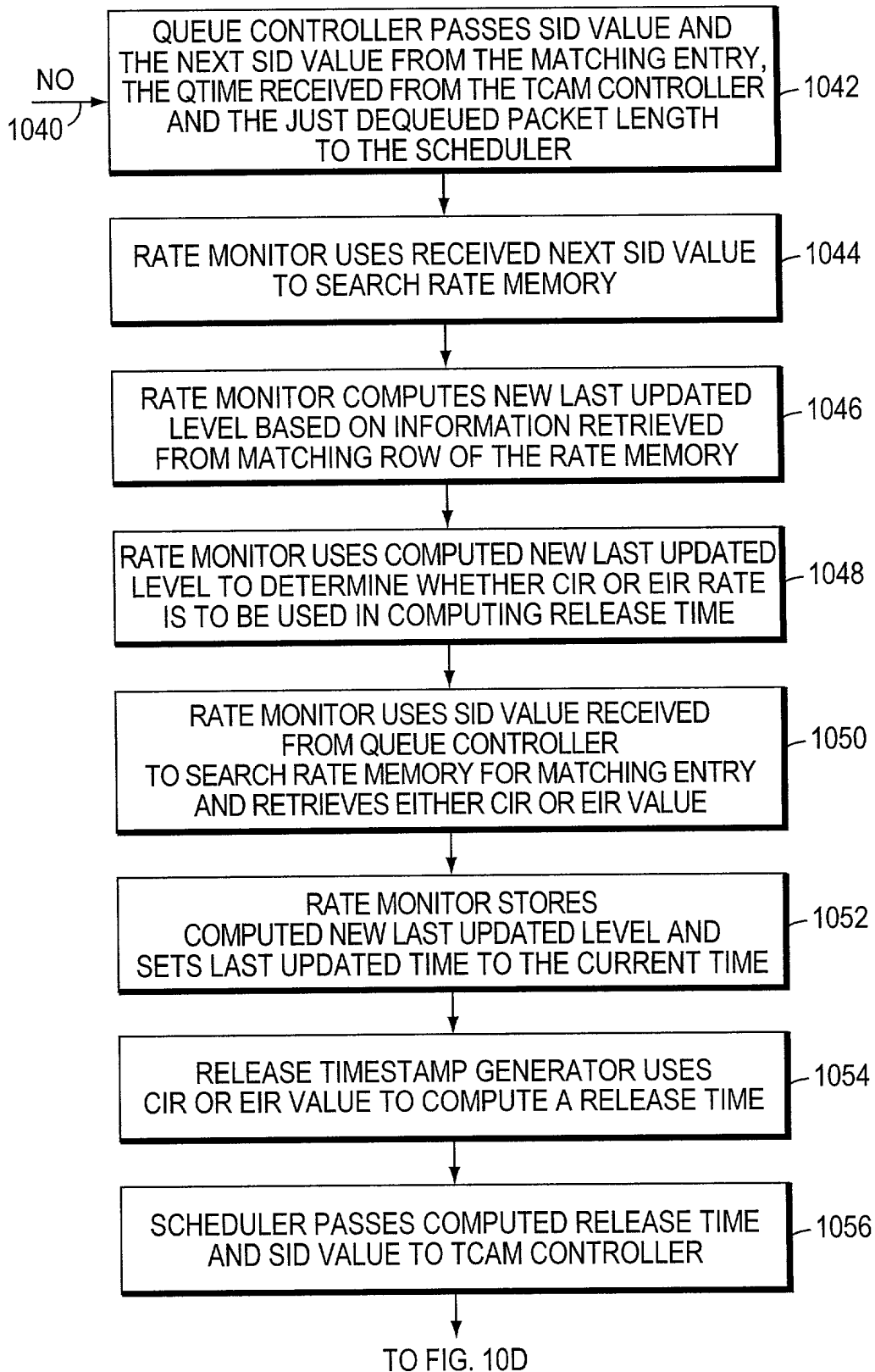


FIG. 10C

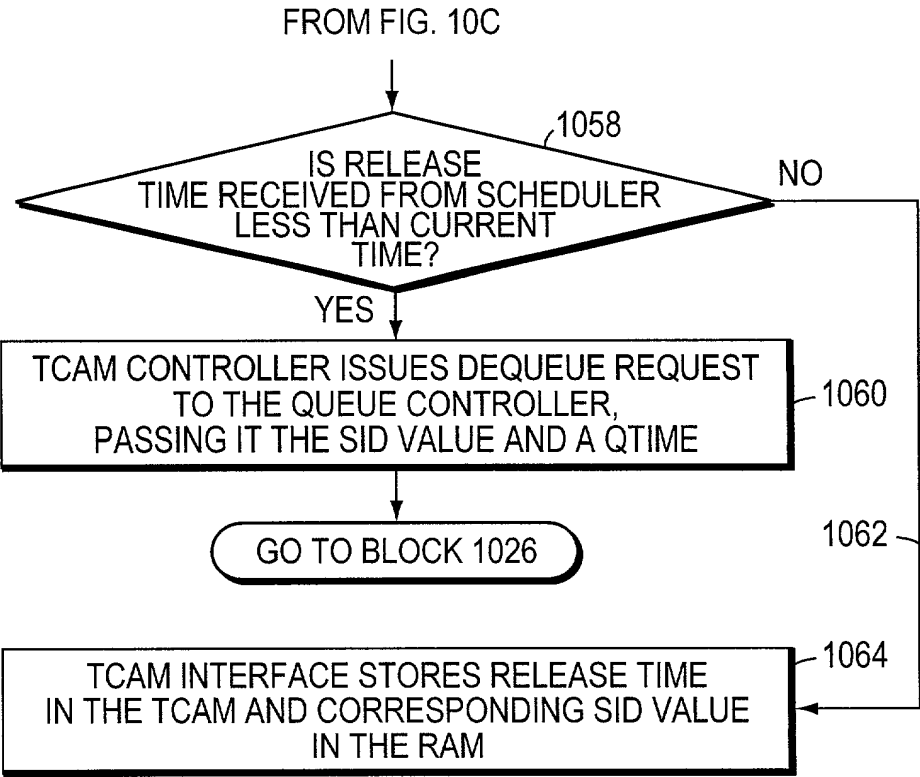


FIG. 10D

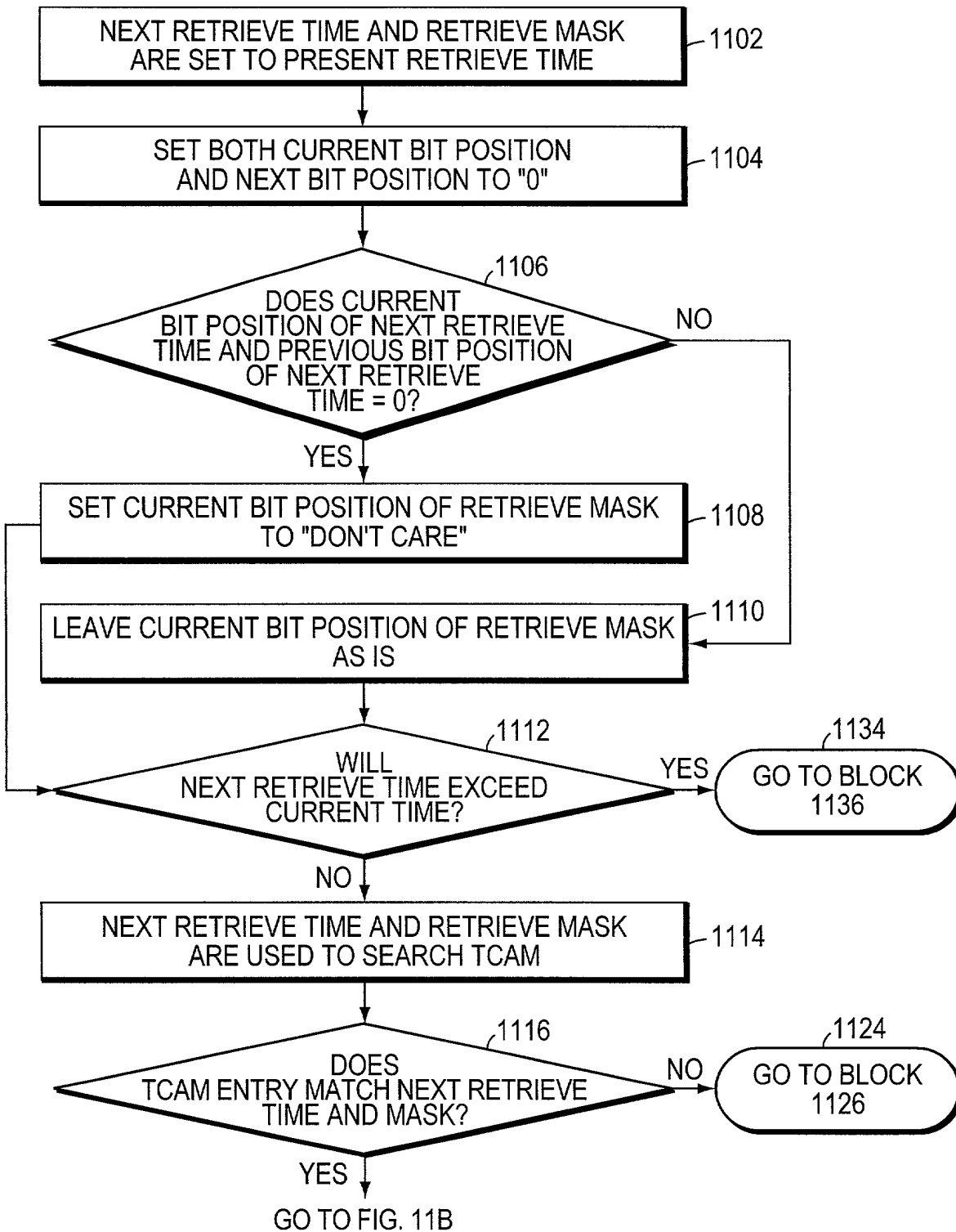


FIG. 11A

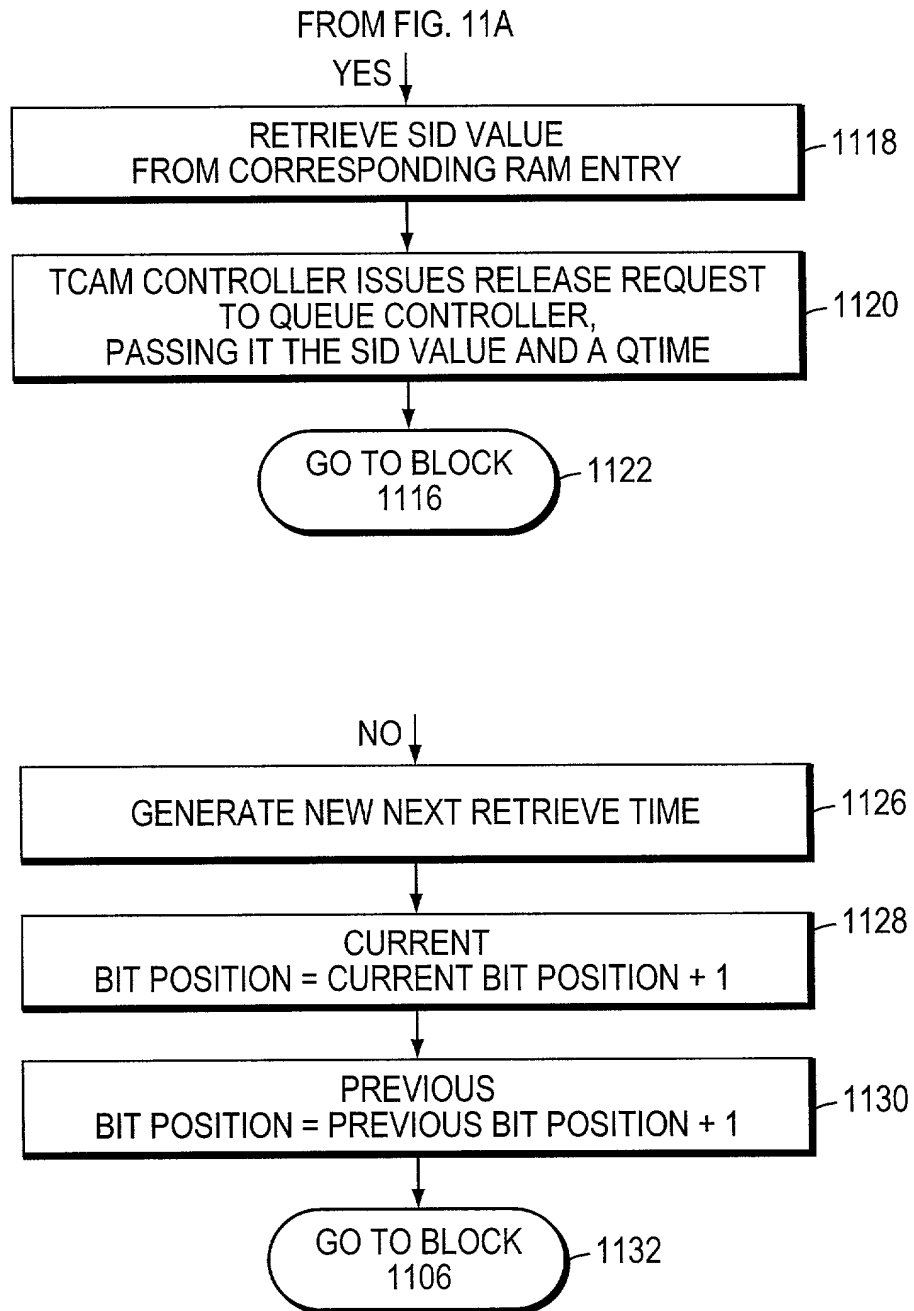


FIG. 11B

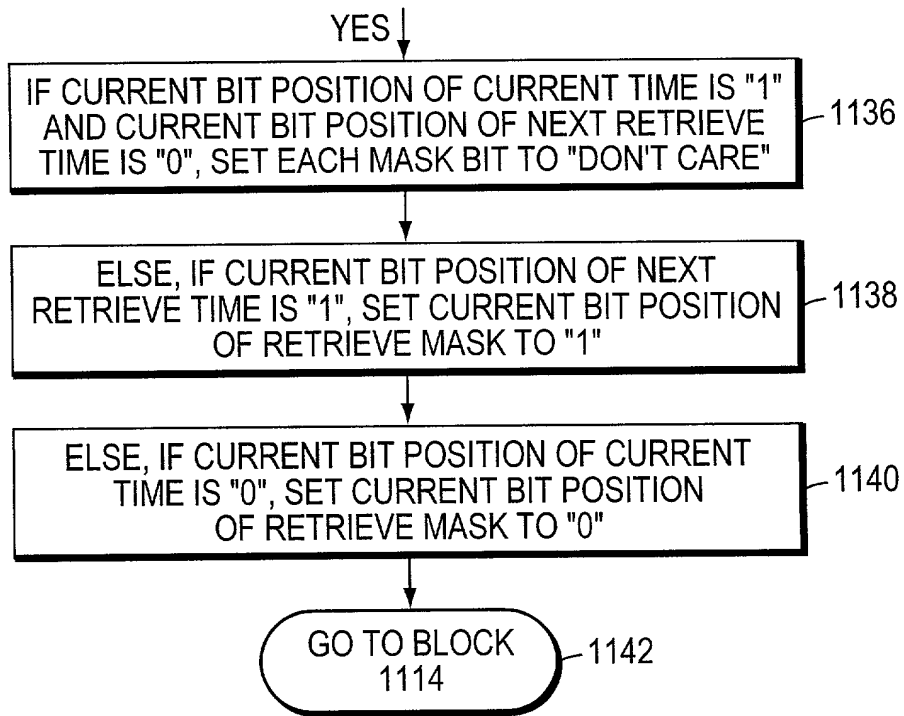


FIG. 11C